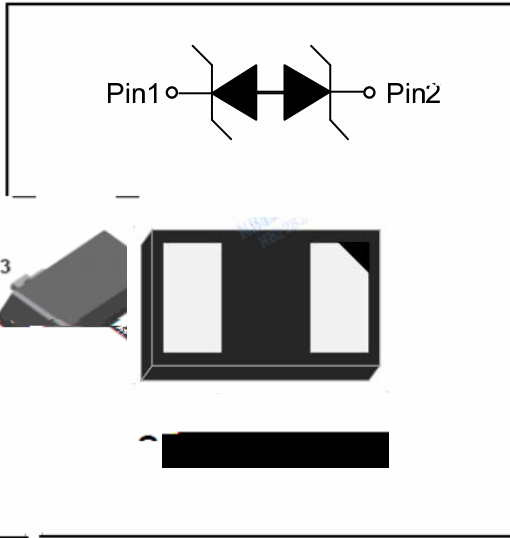


## 1-Line, Bi-directional, Transient Voltage Suppressor



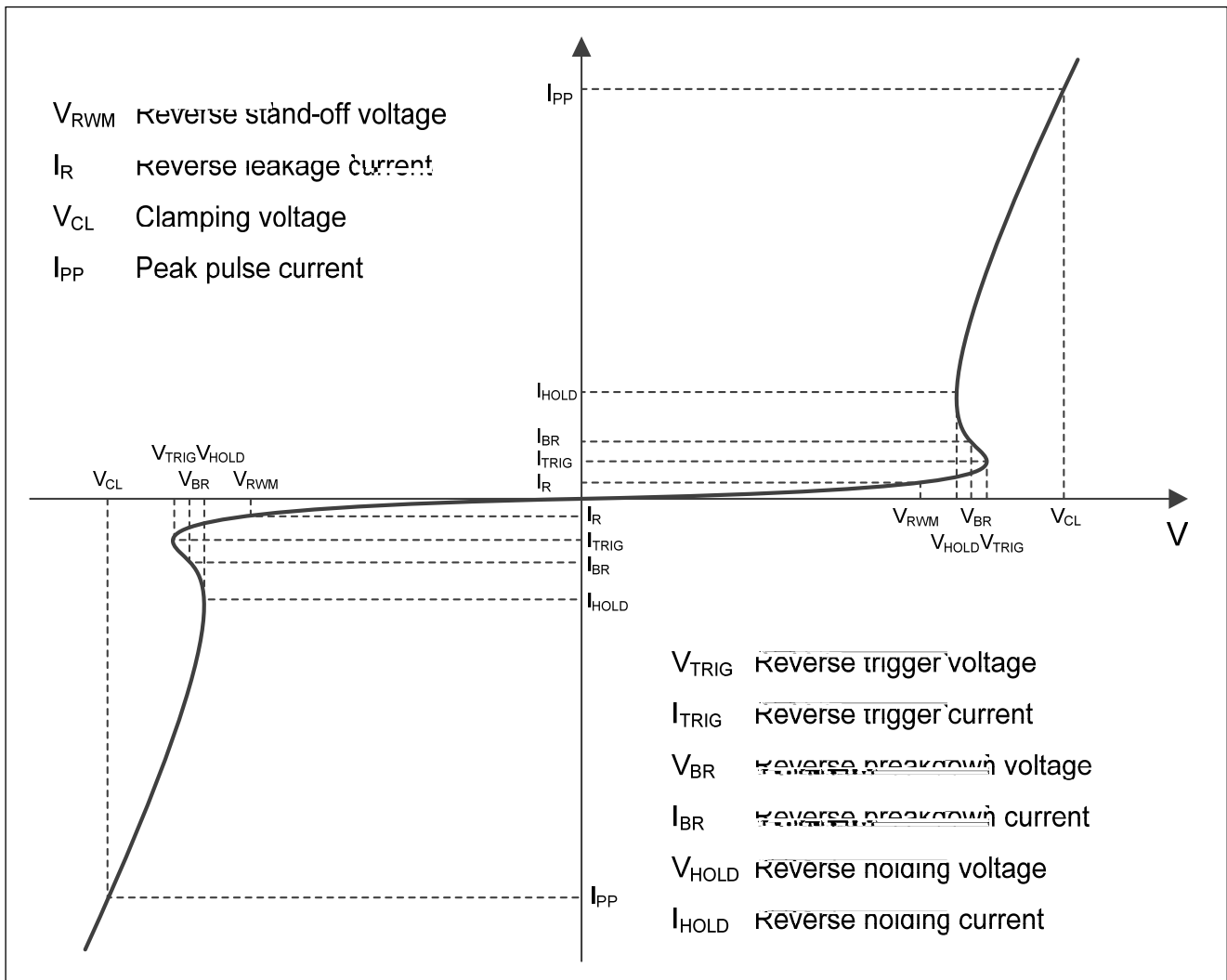
### Features

- Stand-off voltage:  $\pm 5V$  Max
- Transient protection for each line according to IEC61000-4-2(ESD):  $\pm 30kV$  (contact)
- IEC61000-4-4 (EFT): 40A (5/50ns)
- IEC61000-4-5(surge): 8A (8/20 $\mu s$ )
- Ultra-low capacitance:  $C_J = 10pF$  typ
- Low leakage current
- Low clamping voltage:  $V_{CL} = 10.0V$  typ. @  $I_{PP} = 16A$  (TLP)
- Solid-state silicon technology

### Mechanical Data

- Package:** DFN1006-2L
- Terminals:** Tin plated leads, solderable per J-STD-002 and JESD22-B102
- Polarity:** No marking on bi-directional types
- Marking:** 5A

### Definitions of electrical characteristics

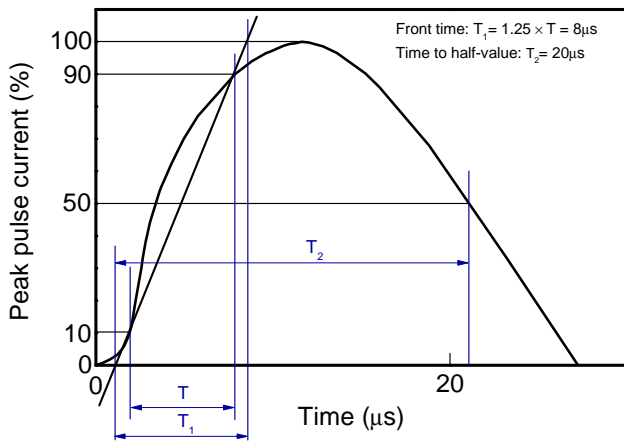






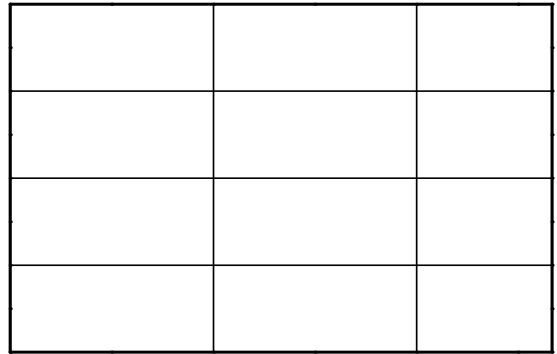
## Characteristics (Typical)

8/20  $\mu$ s waveform per IEC61000-4-5



Clamping voltage vs. Peak pulse current

Contact discharge current waveform per IEC61000-4-2



Capacitance vs. Reverse voltage

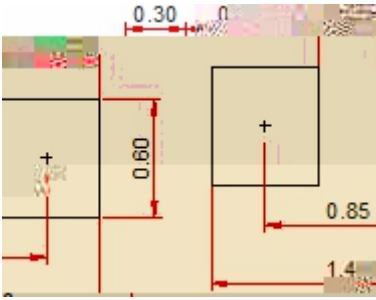
Non-repetitive peak pulse power vs. Pulse time

Power derating vs. Ambient temperature





## Recommend land pattern (Unit:mm)



**Notes:**

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met



# ESD5V0LB

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## Disclaimer

The information presented in this