





# YJB270G10H

Electrical Characteristics ( $T_J=25$  unless otherwise noted)

Parameter	Symbol	Conditions	Min	
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# YJB270G10H

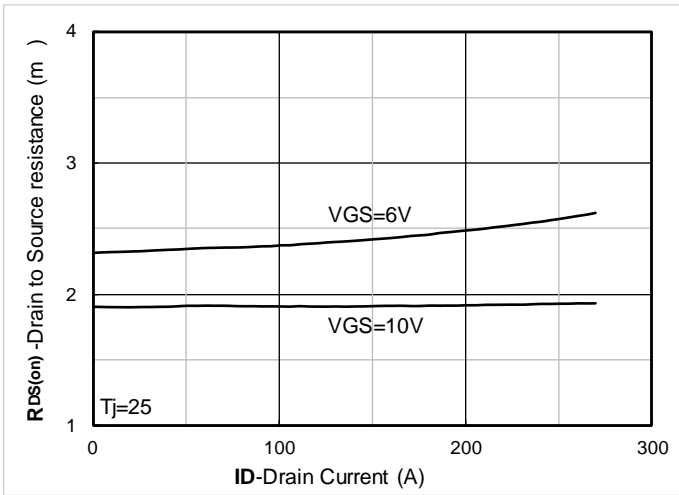


Figure 7. RDS(on) VS Drain Current

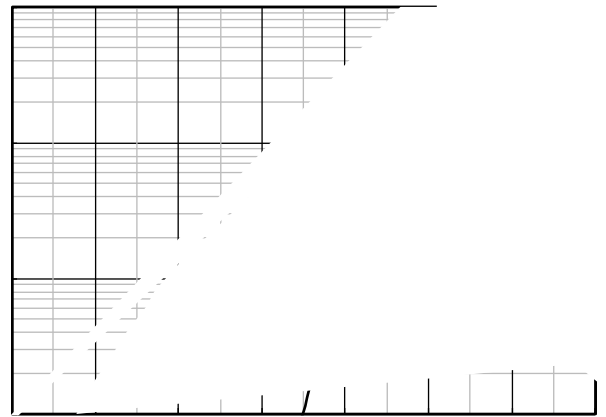


Figure 8. Forward characteristics of reverse diode

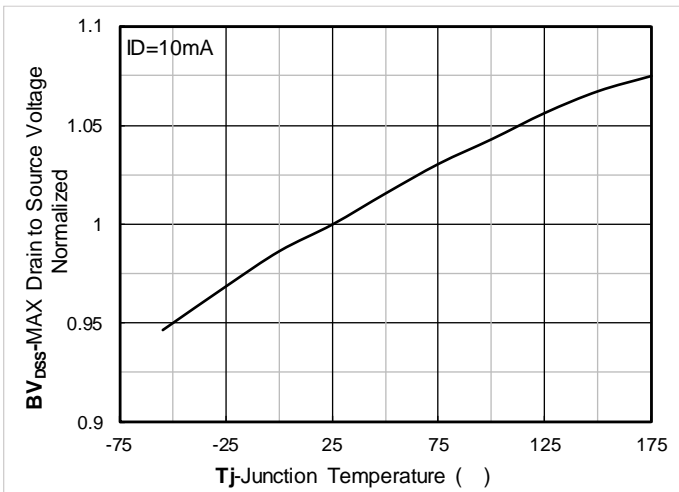


Figure 9. Normalized breakdown voltage

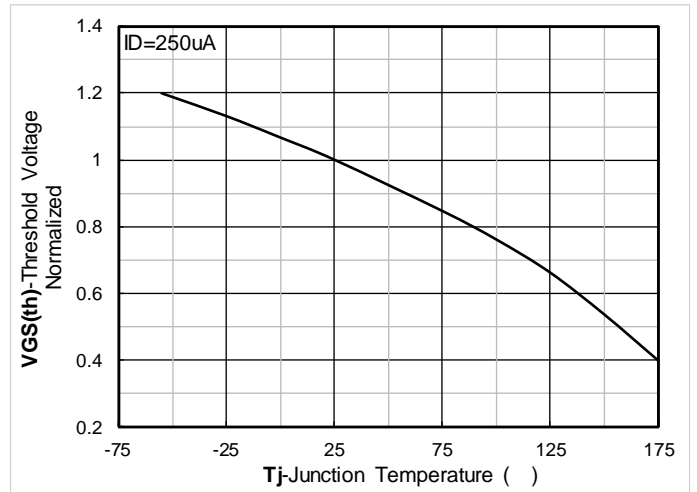


Figure 10. Normalized Threshold voltage

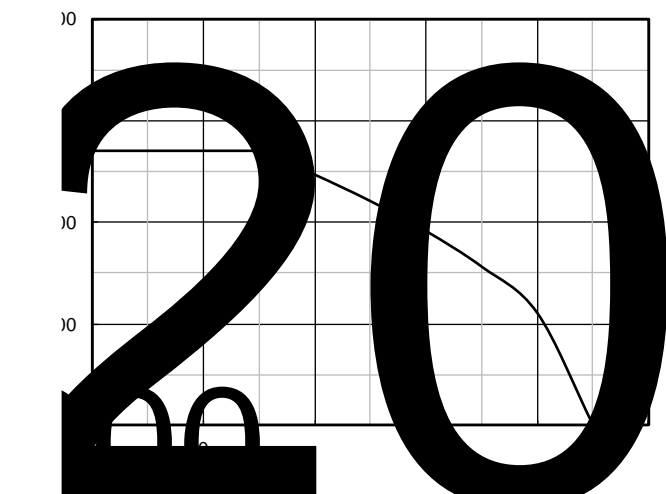


Figure 11. Current dissipation

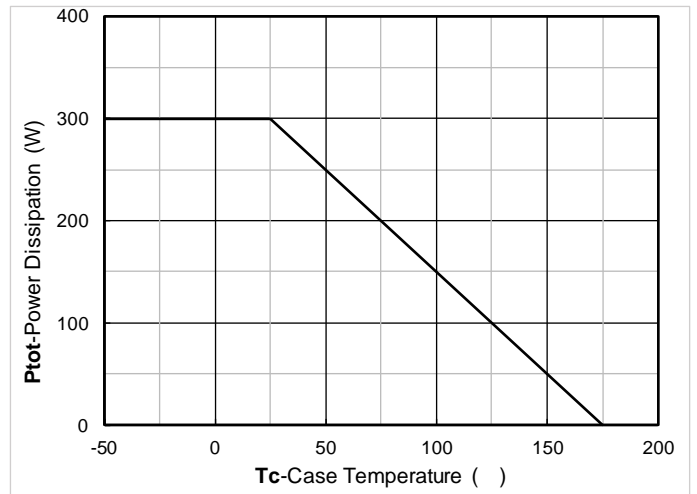


Figure 12. Power dissipation



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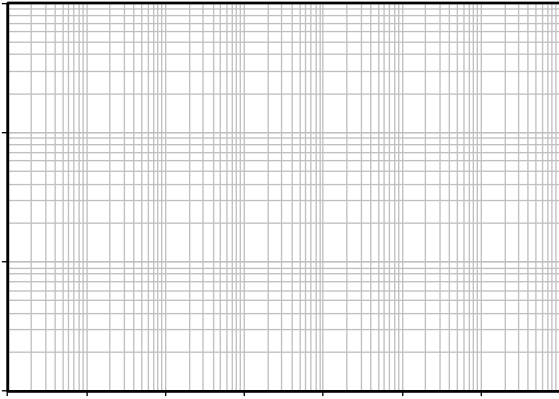


Figure 13. Maximum Transient Thermal Impedance

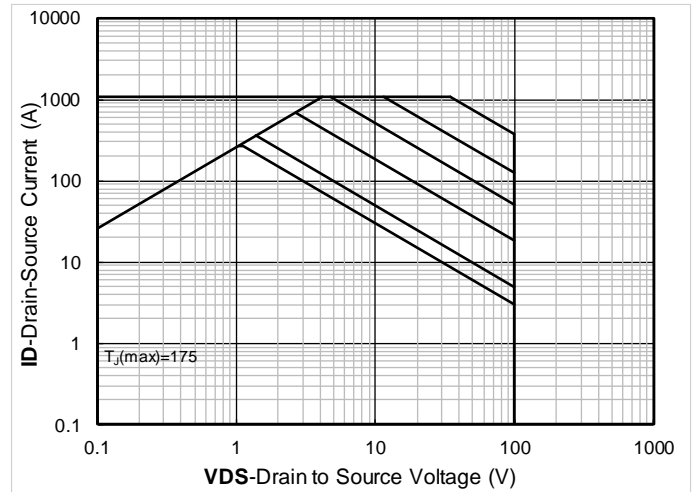


Figure 14. Safe Operation Area

## Test Circuits & Waveforms

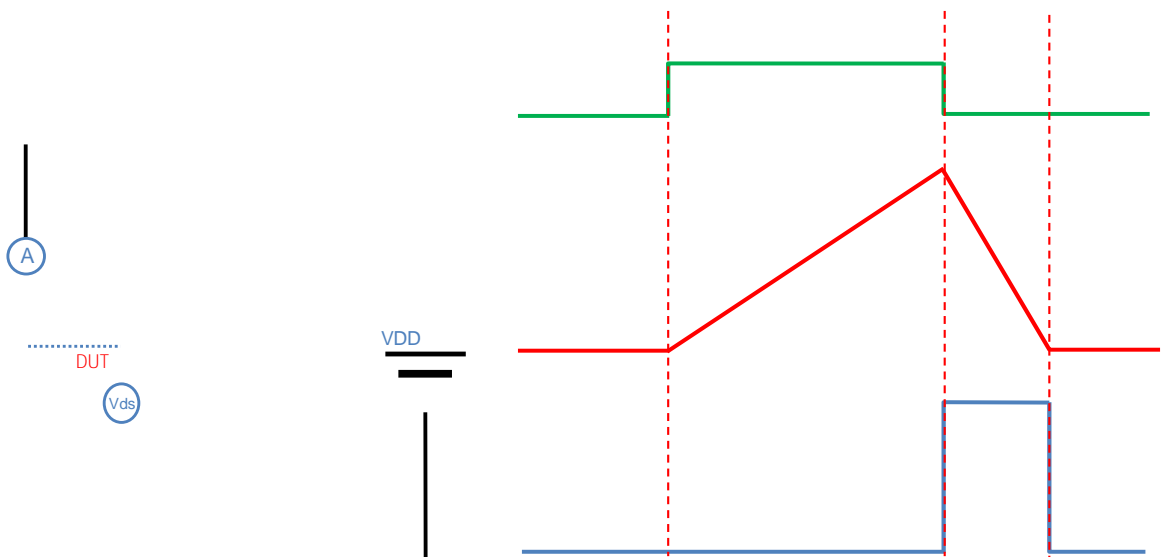


Figure A. Unclamped Inductive Switching (UIS) Test Circuit & Waveform

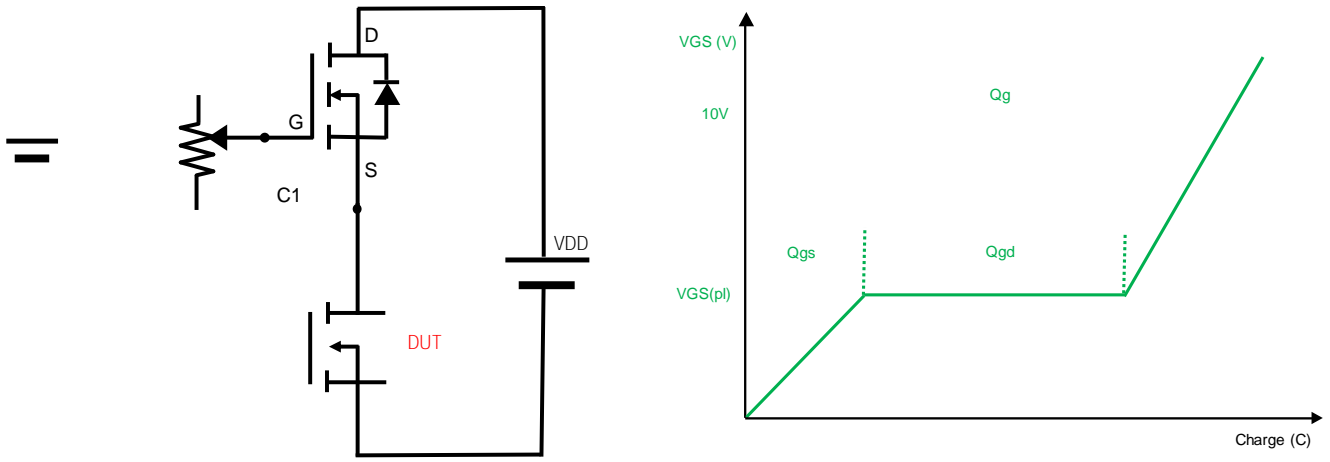
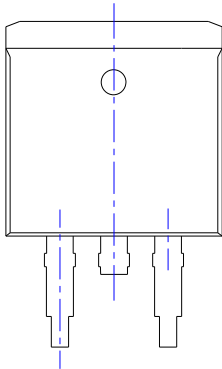


Figure B. Gate Charge Test Circuit & Waveform



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## TO-263-B Package information



**NOTE:**

- 1.PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
- 2.TOLERANCE 0.1mm UNLESS OTHERWISE SPECIFIED.
- 3.THE PAD LAYOUT IS FOR REFERENCE PURPOSES ONLY.

UNIT mmmmD TJETO 2 J 50-A-E39-211E0-4950P-2K567855-520-234E70270-201P3855629-9452333570270.9459S300

## Marking Information



**Note**

- 1. All marking is at middw( 32.6455)BT w( th-3(32.645p)BTroi)9(d)9(u)-2(a)-6(t w( b)-2(o)

