



# YJG110G08HR

## N-Channel Enhancement Mode Field Effect Transistor

### Product Summary

$V_{DS}$	80V
$I_D$	110A
$R_{DS(ON)}$ ( at $V_{GS}=10V$ )	3.9m
$R_{DS(ON)}$ ( at $V_{GS}=6V$ )	6m
100% EAS Tested	
100% $V_{DS}$ Tested	

### General Description

Split gate trench MOSFET technology  
Excellent package for heat dissipation  
High density cell design for low  $R_{DS(ON)}$   
Moisture Sensitivity Level 3  
Epoxy Meets UL 94 V-0 Flammability Rating  
Halogen Free

### Applications

Power switching application  
Uninterruptible power supply  
DC-DC convertor

### Absolute Maximum Ratings ( $T_A=25$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-source Voltage	$V_{DS}$	80	V
Gate-source Voltage	$V_{GS}$	$\pm 20$	V

$T_A=25$

Drain Current

$I_D$



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## Electrical Characteristics ( $T_J=25$ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
<b>Static Parameter</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	80	-	-	V





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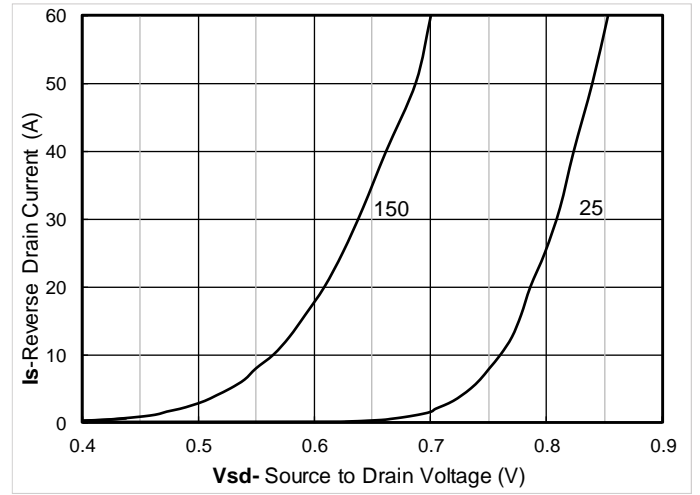
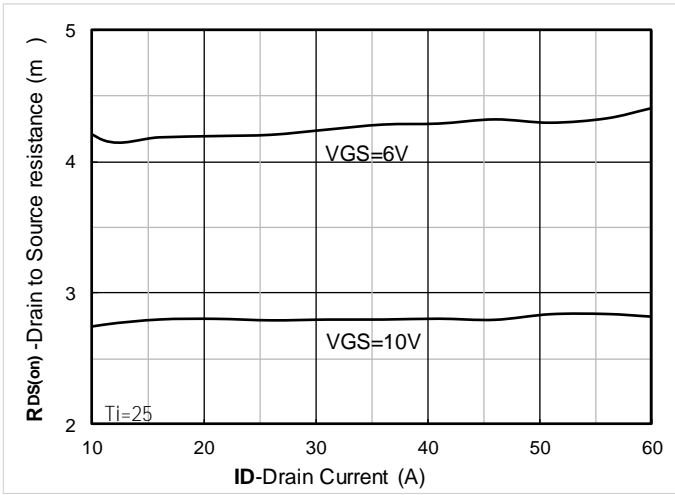


Figure 7.  $R_{DS(on)}$



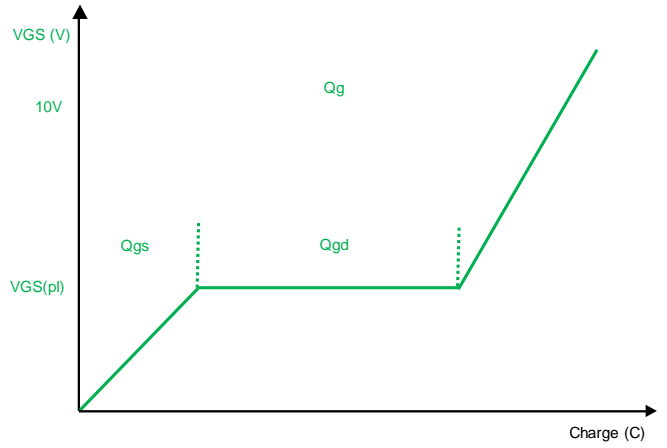
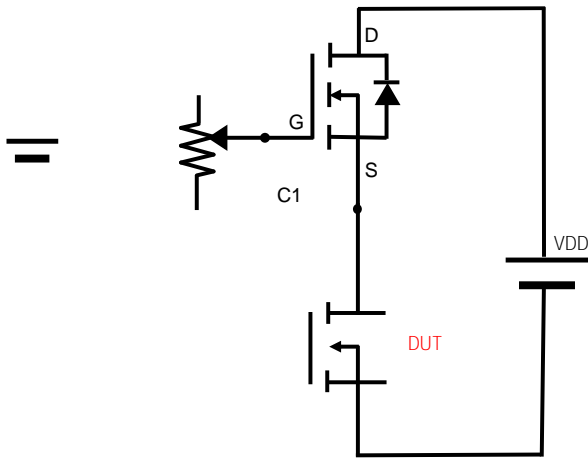


Figure B. Gate Charge Test Circuit & Waveform

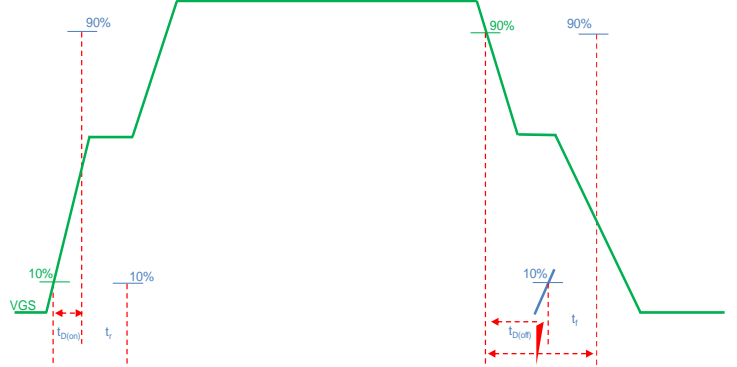


Figure C. Resistive Switching Test Circuit & Waveform

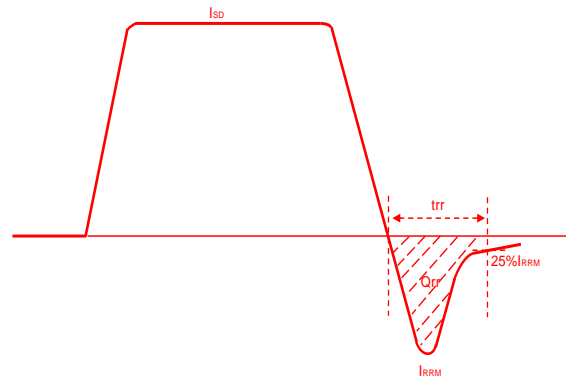
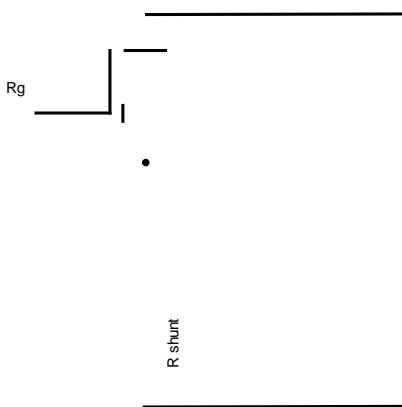


Figure D. Diode Recovery Test Circuit & Waveform

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