

Silicon Carbide Schottky Diode

V_{RRM}	650 V
I_F @ 135°C	10 A
Q_C	25 nC

Features

- Positive temperature coefficient
- Temperature-independent switching
- Maximum working temperature at 175 °C
- Unipolar devices and zero reverse recovery current
- Zero forward recovery voltage
- Essentially no switching losses
- Reduction of heat sink requirements
- High-frequency operation
- Reduction of EMI

Typical Applications

Typical applications are in power factor correction(PFC), solar inverter, uninterruptible power supply, motor drives, photovoltaic inverter, electric car and charger.

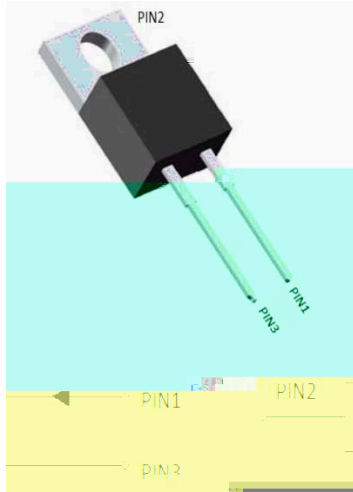
Mechanical Data

Package: TO-220AC

Molding compound meets UL 94 V-0 flammability rating, RoHS-compliant, halogen-free

Terminals: Tin plated leads

Polarity: As marked



Maximum Ratings ($T_c=25$ Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	VALUE
Device marking code			D106506PQG2
Reverse voltage (repetitive peak) @ $T_j=25^\circ\text{C}$	V_{RRM}	V	650
Reverse voltage (Surge Peak) @ $T_j=25^\circ\text{C}$	V_{RSM}	V	650
Reverse voltage (DC) @ $T_j=25^\circ\text{C}$	V_{DC}	V	650
Continuous forward current @ $T_c=25^\circ\text{C}$	I_F	A	21
Continuous forward current @ $T_c=135^\circ\text{C}$			10
Continuous forward current @ $T_c=157^\circ\text{C}$			6
Non-repetitive peak forward surge current @ $T_c=25^\circ\text{C}$, $t_p=10\text{ms}$, Half Sine Wave	I_{FSM}	A	65
Power Dissipation @ $T_c=25^\circ\text{C}$	P_{TOT}	W	84
Power Dissipation @ $T_c=110^\circ\text{C}$			36
i^2t Value @ $T_c=25^\circ\text{C}$, $t_p=10\text{ms}$	i^2t	A^2S	21
Operating junction and Storage temperature range	T_j, T_{stg}	$^\circ\text{C}$	-55 to +175



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Electrical Characteristics

PARAMETER	SYMBOL	UNIT	TEST CONDITIONS	Typ.	Max.
Forward voltage drop	V_F	V	$I_F=6A, T_J=25^\circ C$	1.31	1.5
			$I_F=6A, T_J=175^\circ C$	1.65	-
Reverse leakage current	I_R	μA	$V_R=650V, T_J=25^\circ C$	0.5	25
			$V_R=650V, T_J=175^\circ C$	5	-
Total capacitive charge	Q_C	nC	V_R		

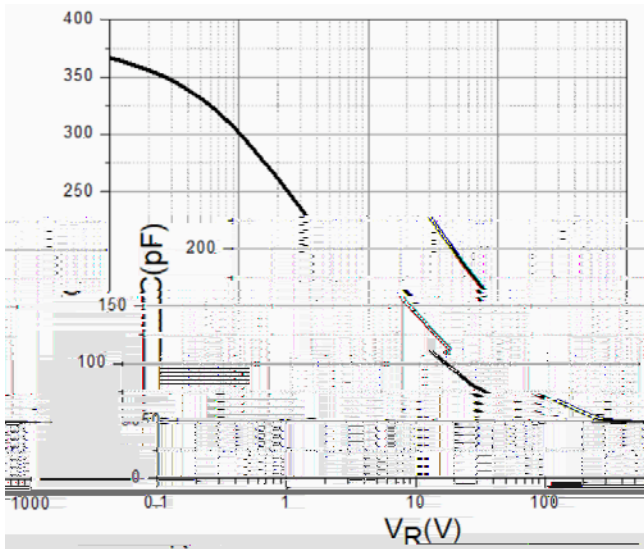


Figure 3. Capacitance vs. Reverse Voltage



Figure 4. Total Capacitance Charge vs. Reverse Voltage

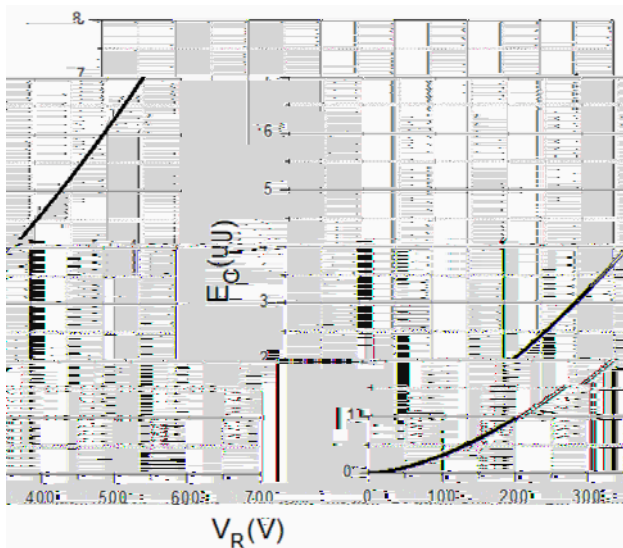


Figure 5. Capacitance Stored Energy

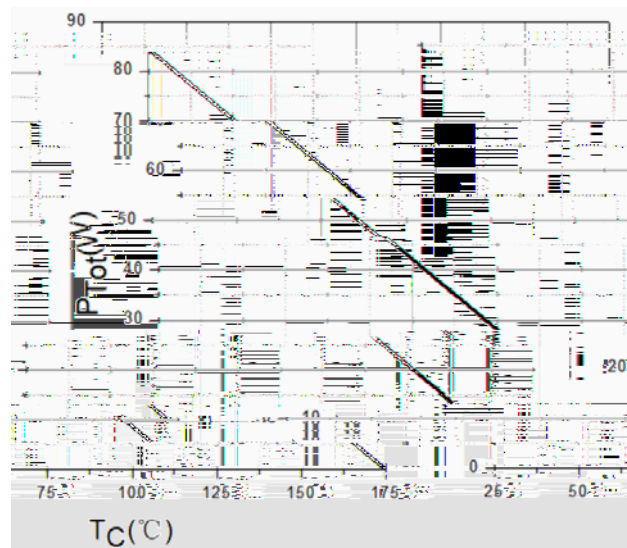


Figure 6. Power Derating

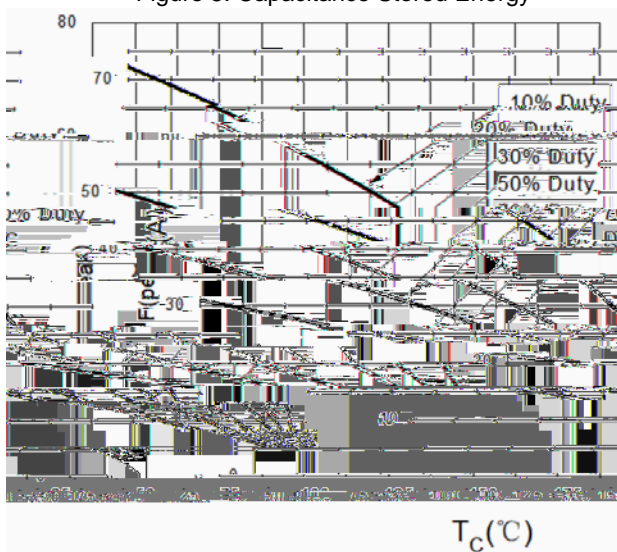


Figure 7. Current Derating

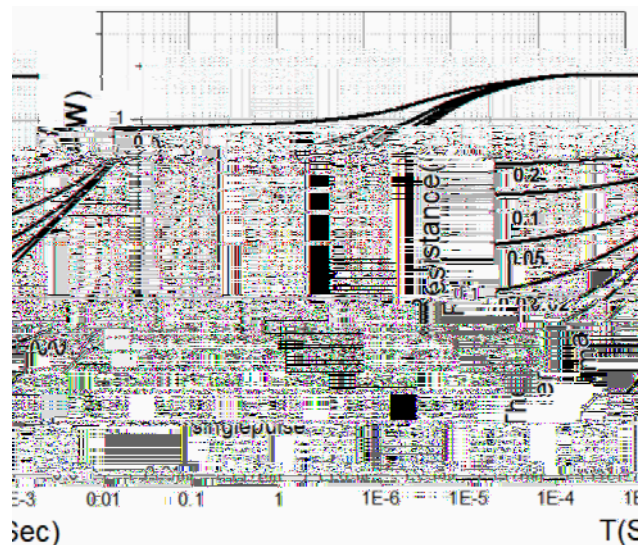
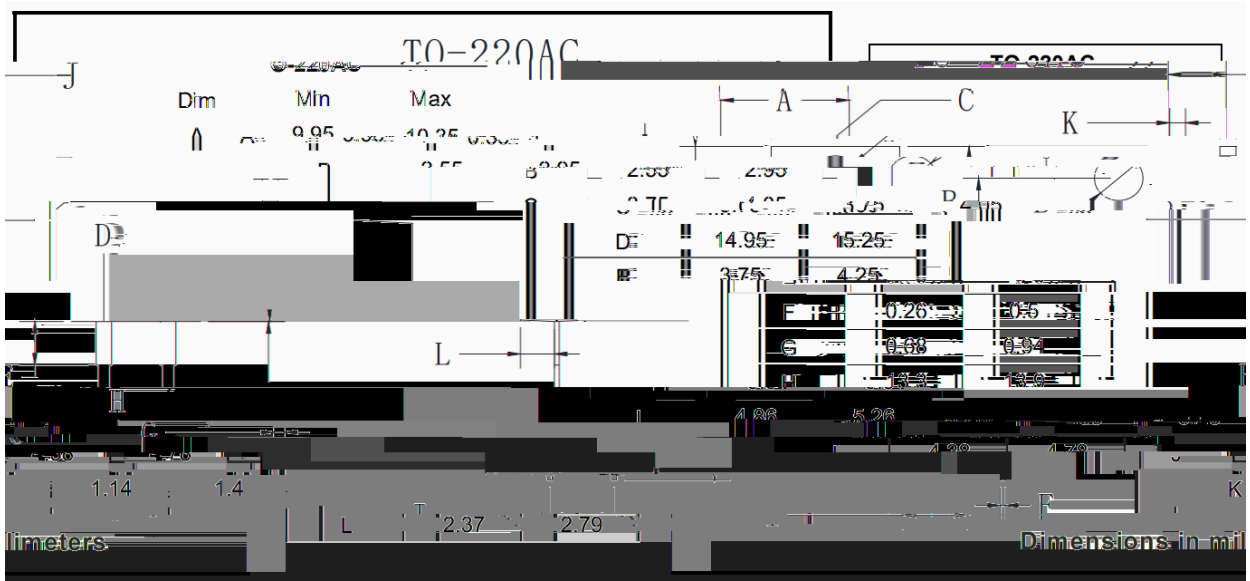


Figure 8. Transient Thermal Impedance



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Outline Dimensions



'LVFODLPHU

7KH LQIRUPDWLRQ SQRHWKLV GRFXPHQW < DQJ]KRX < DQJMEHFKHQWURQL&R /VZ \DQJMLH F