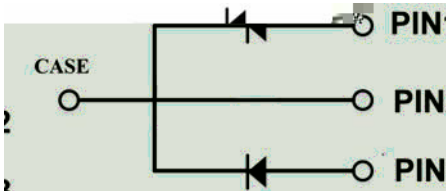
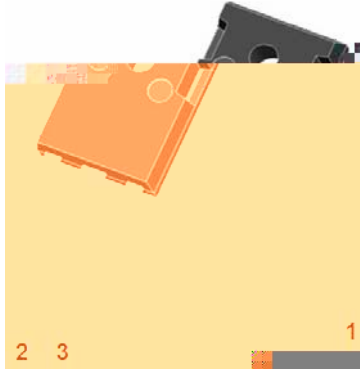


$V_{RRM}$	650V
$I_F$ 135°C	26A <sup>(2)</sup>
$Q_C$	60nC <sup>(2)</sup>



### Typical Applications

Typical applications are in power factor correction(PFC), solar inverter, uninterruptible power supply, motor drives, photovoltaic inverter, electric car and charger.

### Mechanical Data

**Package:** TO-247AB

Molding compound meets UL 94 V-0 flammability rating, RoHS-compliant, halogen-free

**Terminals:** Tin plated leads

**Polarity:** As marked

### Maximum Ratings ( $T_c=25$ Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	VALUE
Device marking code			D106520NCTQG2
Reverse voltage (repetitive peak) @ $T_j=25^\circ\text{C}$	$V_{RRM}$	V	650
Reverse voltage (Surge Peak) @ $T_j=25^\circ\text{C}$	$V_{RSM}$	V	650
Reverse voltage (DC) @ $T_j=25^\circ\text{C}$	$V_{DC}$	V	650
Continuous forward current @ $T_c=25^\circ\text{C}$			27/54
Continuous forward current @ $T_c=135^\circ\text{C}$	$I_F$	A	13/26
Continuous forward current @ $T_c=150^\circ\text{C}$			10/20
Non-repetitive peak forward surge current @ $T_c=25^\circ\text{C}$ , $t_p=10\text{ms}$ , Half Sine Wave	$I_{FSM}$	A	70 <sup>(1)</sup>
Power Dissipation @ $T_c=25^\circ\text{C}$			112/230
Power Dissipation @ $T_c=110^\circ\text{C}$	$P_{TOT}$	W	48/100
$i^2t$ Value @ $T_c=25^\circ\text{C}$ , $t_p=10\text{ms}$	$i^2dt$	A <sup>2</sup> S	32 <sup>(1)</sup>
Operating junction and Storage temperature range	$T_j, T_{slg}$	°C	-55 to +175

(1) Per Leg, (2) Per Device

v(OHFWULFDO &KDUDFWHULVWLFV

3\$5\$07(7(5	6<0%2	81,7	7(67 &21',7,21	7\5	0D[
)RUZDUG YROWDJH GURS	)		) \$ 7 f&		
			) \$ 7 f&		
5HYHUVH OHDNDJH FXUUHQW	5		9 <sub>5</sub> 9 7 f&		
			9 <sub>5</sub> 9 7 f&		
7RWDO FDSDFLWLYH FKDUJH	&		9 <sub>5</sub> 9 7 f& 4 & 9 G9		
7RWDO FDSDFLWDQFH			9 <sub>5</sub> 9 1 0+=		
			9 <sub>5</sub> & 9 1 S0+=		
			9 <sub>5</sub> 9 1 0+=		
&DSDFLWDQFH 6WRUHG (QHUV\	&		5 9 -		

v7KHUPDO &KDUDFWHULVWLFV RWKHUZLVH VSHFLILHG

3\$5\$0(7(5	6<0%2/	81,7	9DOXH
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7KHUPDO UHVLVWDQFH

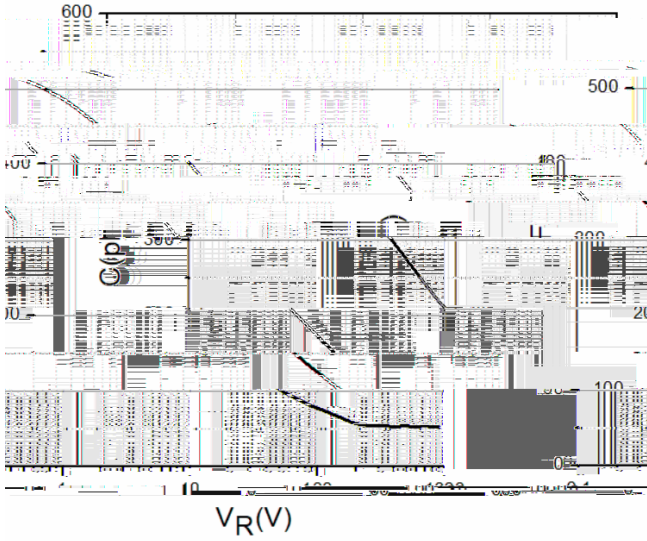


Figure 3. Capacitance vs. Reverse Voltage

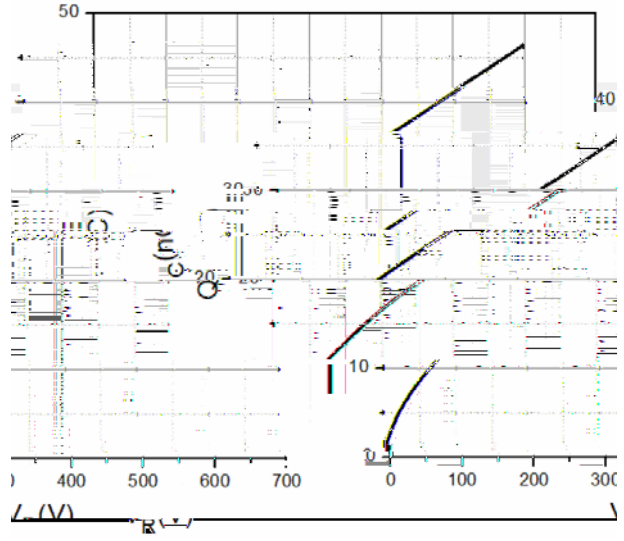


Figure 4. Total Capacitance Charge vs. Reverse Voltage

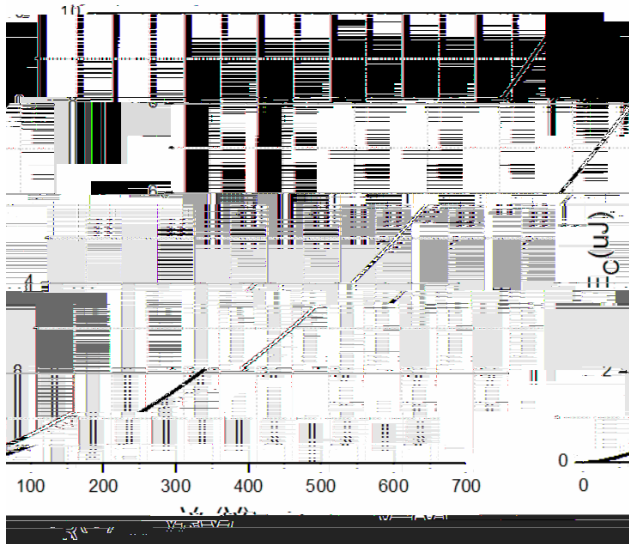


Figure 5. Capacitance Stored Energy

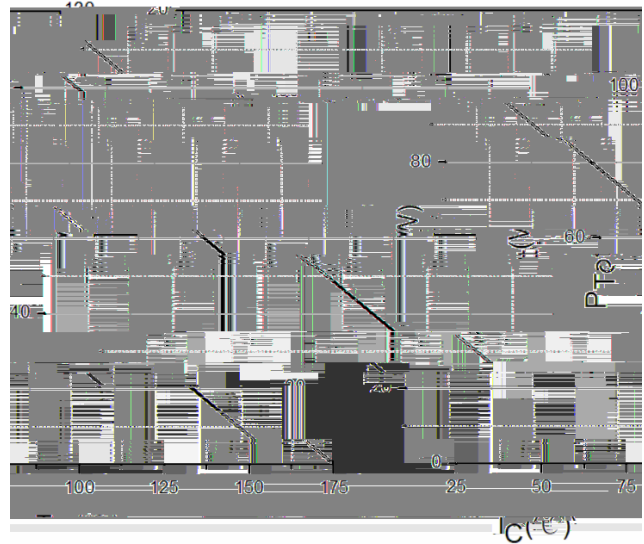


Figure 6. Power Derating

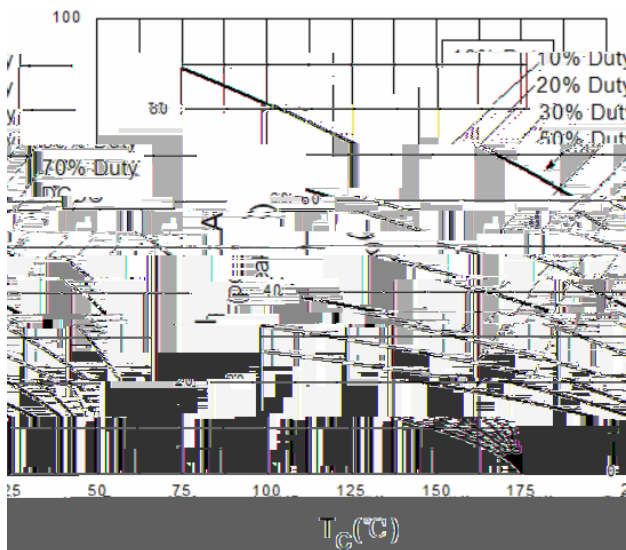


Figure 7. Current Derating

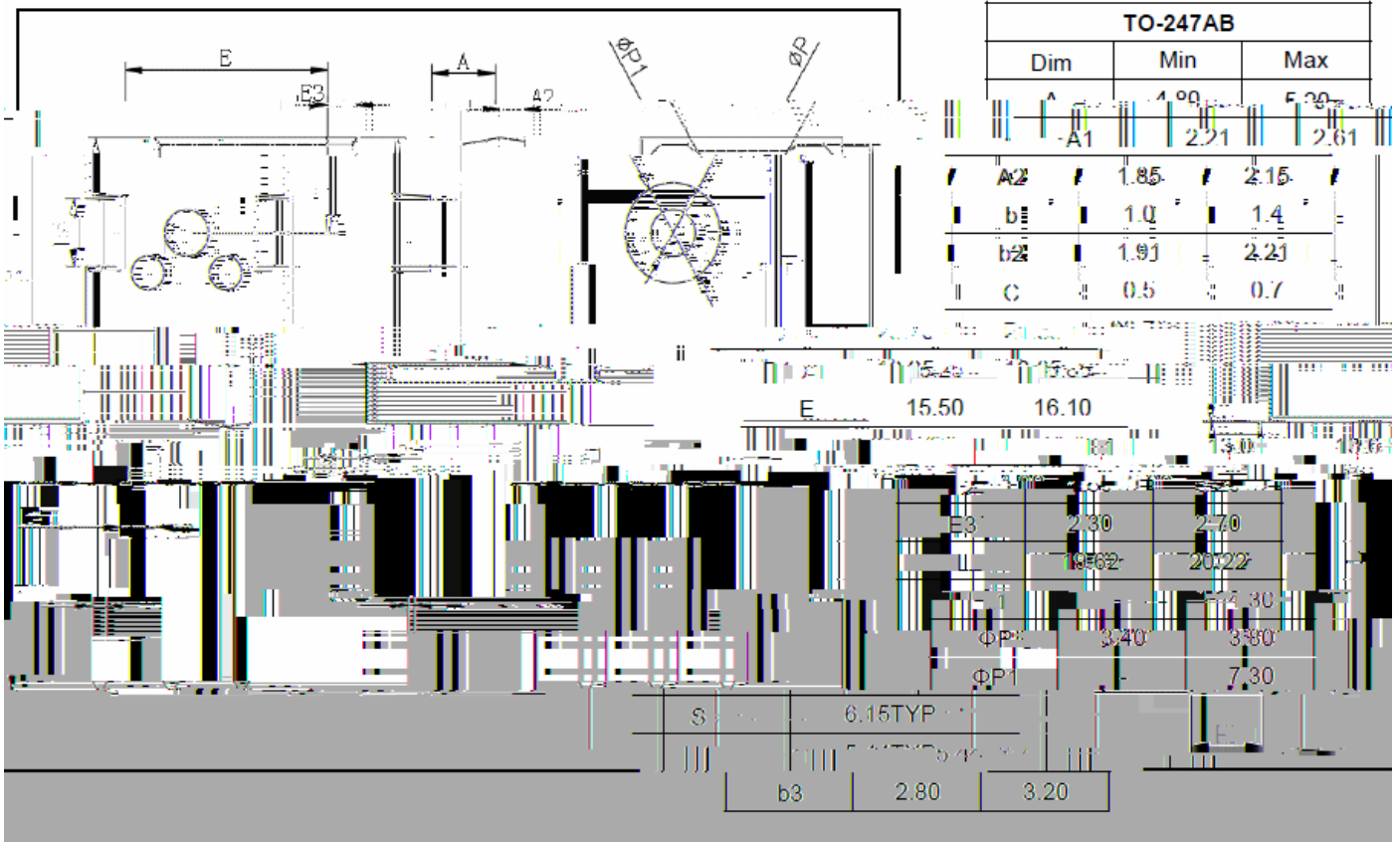


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Typical Characteristics (Device)



Outline Dimensions





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