



## PNP Digital Transistors (Built-in Resistors)

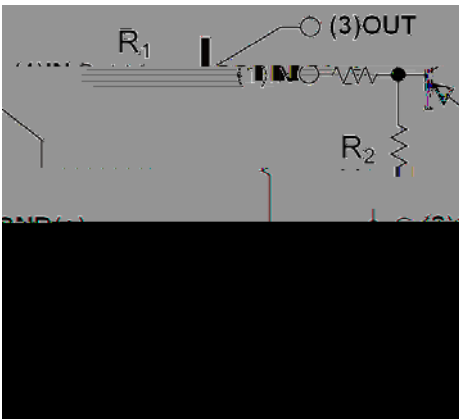
### Features

Moisture Sensitivity Level 1  
Surface mount package ideally Suited for Automatic  
Insertion  
Halogen-free

### Mechanical Data

**Package:** SOT-523  
**Terminals:** Tin plated leads, solderable per  
J-STD-002 and JESD22-B102  
**Marking:** E11

### Equivalent circuit



### Maximum Ratings (Ta=25 Unless otherwise specified)

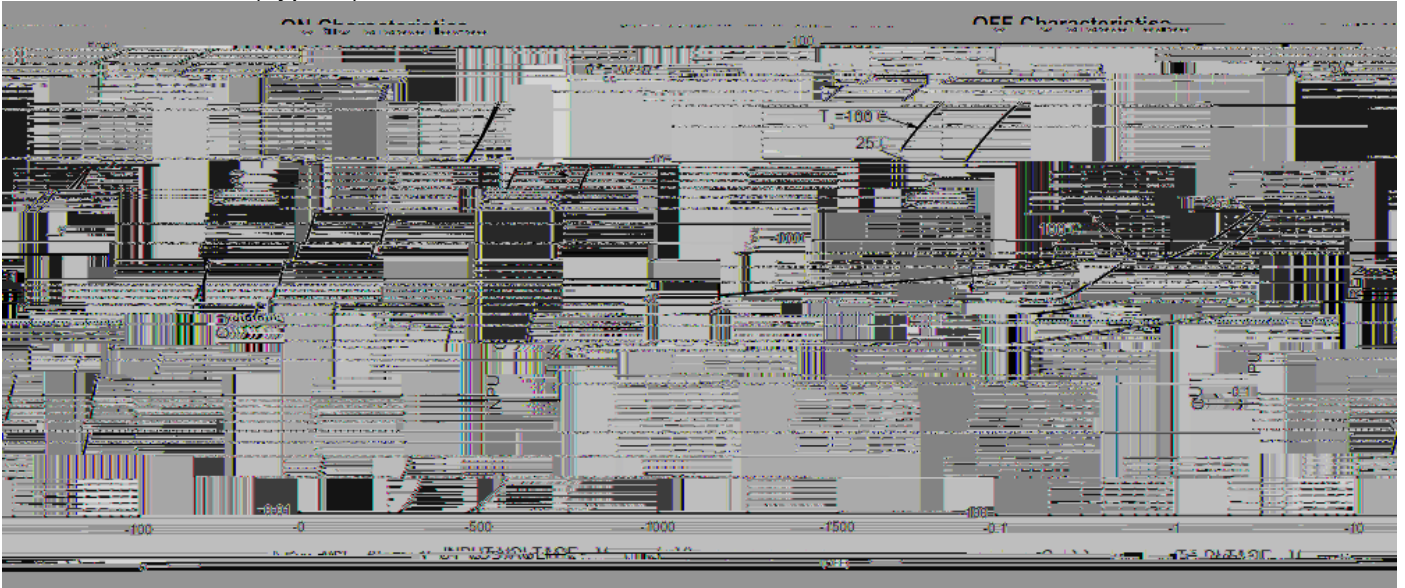
ITEM	SYMBOL	UNIT	CONDITIONS	VALUE
Supply Voltage	$V_{CC}$	V		-50
Input Voltage	$V_{IN}$	V		-10 to +5
Output Current	$I_o$	mA		-100
Power Dissipation	$P_D$	mW		150
Junction Temperature	$T_J$			150





# DTA113ZE

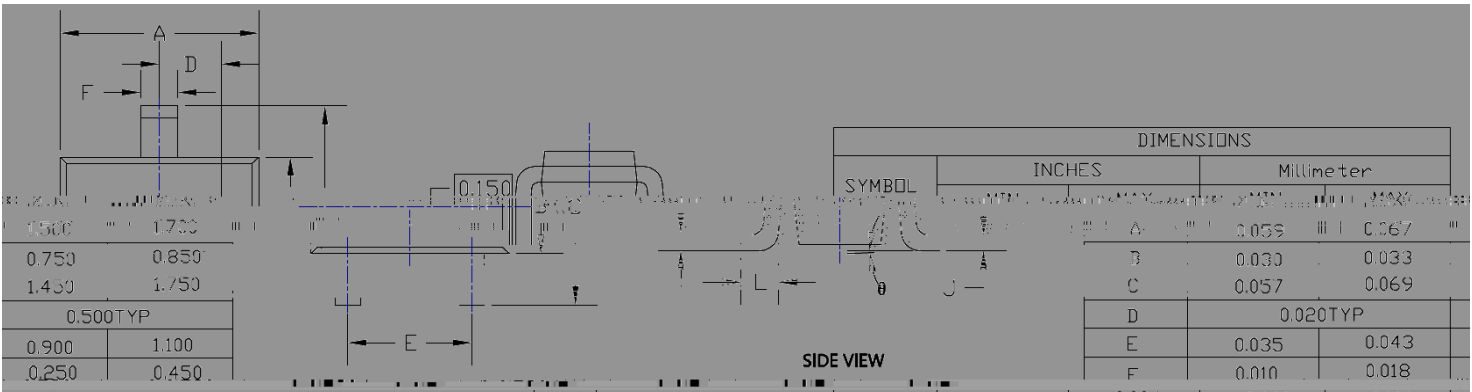
## Characteristics (Typical)





# DTA113ZE

## SOT-523 Package Outline Dimensions



Note:

1. All dimensions are in millimeters (mm) unless otherwise specified.



## Disclaimer

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The product listed herein is designed to be used with ordinary electrical equipment. The product is not intended for use in hazardous or explosive environments.