



UMH9N

Dual NPN Digital Transistors (Built-in Resistors)

Features

Epoxy meets UL-9 (0-flamma V ility rating)



UMH9N

Maximum Ratings (Ta 1 &) Unless other k ise specified

ITEM	SYMBOL	UNIT	CONDITIONS	VALUE
Supply Voltage	V_{cc}	V) \$
Input Voltage	V_i	V		-6 to Ž (\$
Output Current	I_o	mA		% \$ \$
Power Dissipation	P_d	mW		%) \$
Storage Temperature Single	T_{stg}			%) \$
Operating Temperature Single	T_{op}			!)) h c Ž %) \$

Electrical Characteristics (Ta 1 &) unless other k ise specified

ITEM	SYMBOL	UNIT	CONDITIONS	MIN	TYP	MAX
Input voltage	$V_{I(off)}$	V	$V_{cc} - 1$ V Ž $I_c = 1$ mA	\$ " 3	-	-
	$V_{I(on)}$	V	V_{om}			

Characteristics (Typical)

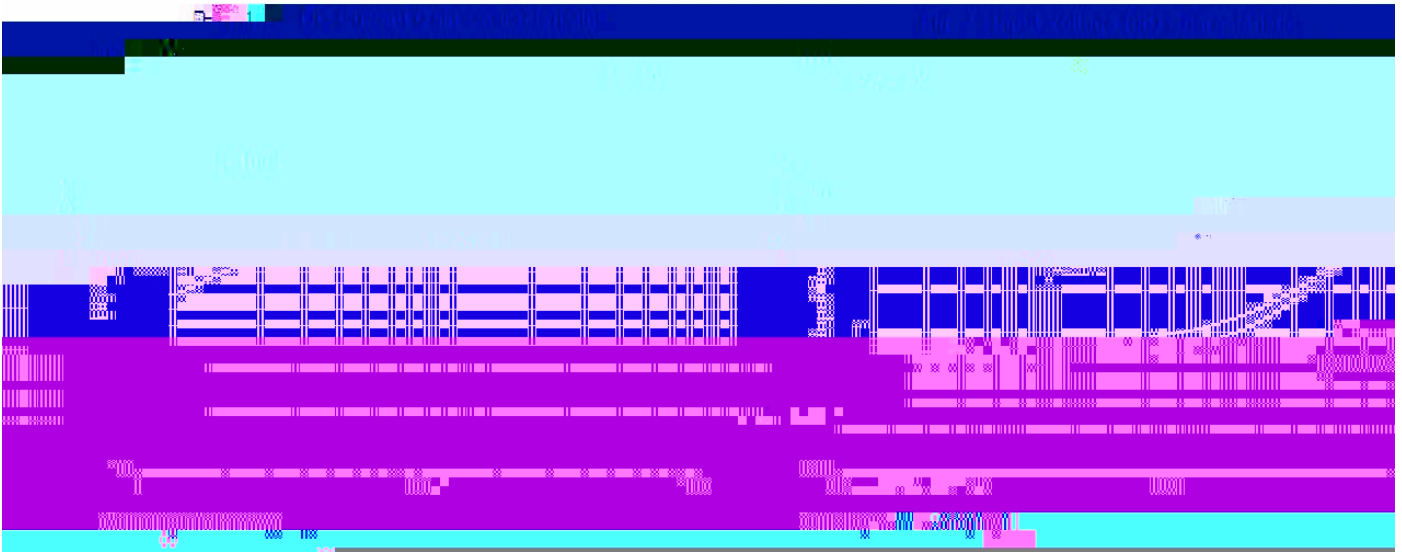
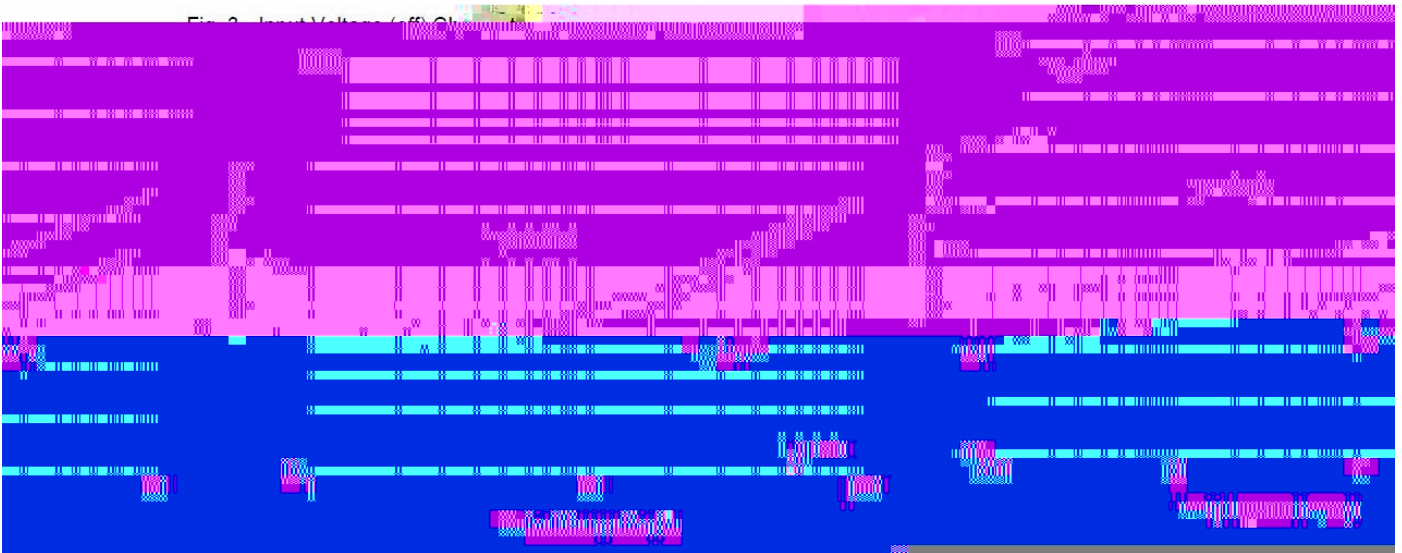


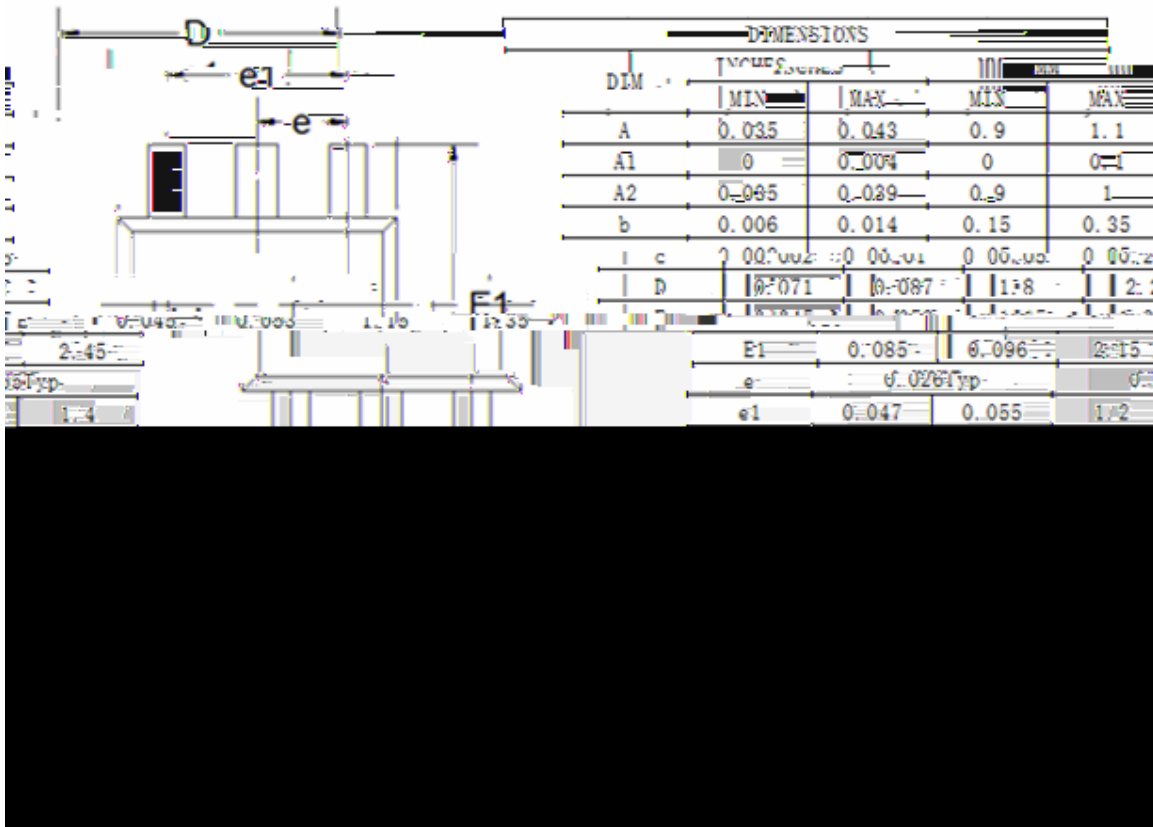
Fig. 2. Input Voltage (V) vs. Output Current (A)



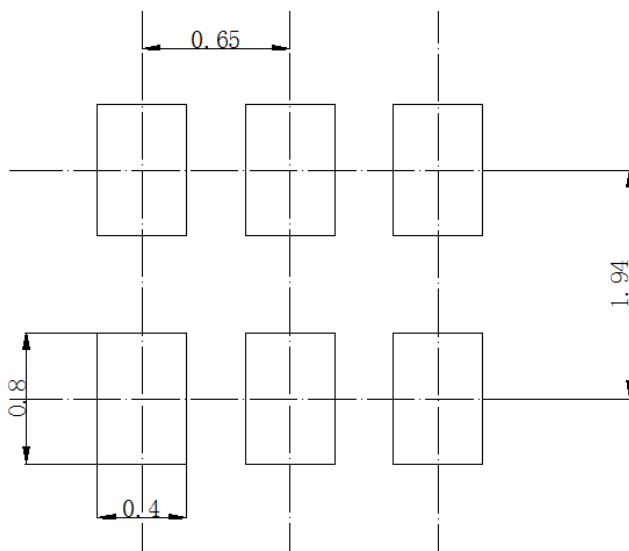


UMH9N

SOT-363 Package Outline Dimensions



SOT-363 Suggested Pad Layout





UMH9N

Disclaimer

The information presented in this document is for reference only. Yangzhou Yangjie Electronic Technology Co., Ltd. reserves the right to make changes without notice for the specification of the products displayed herein to improve reliability, functionality, or otherwise.

The product listed herein is designed to be used with other electronic equipment or devices and not designed to be used ki
