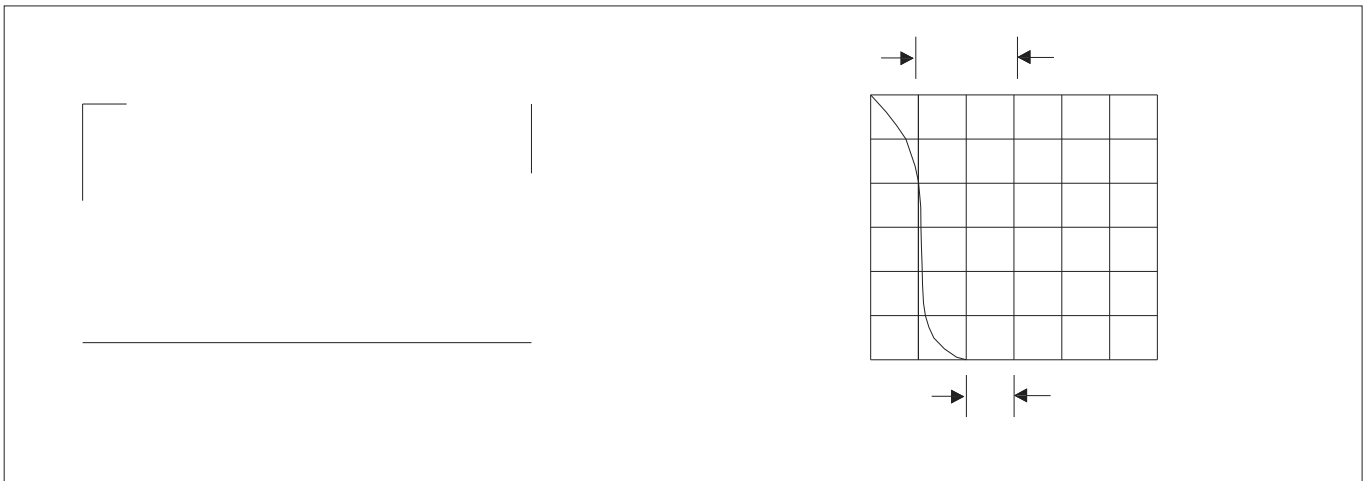






FIG.5: Diagram of circuit and Testing wave form of reverse recovery time







**Disclaimer**