



AUI]a i a 'FUh]b[g (Tj Unless otherwise specified

D5F5A9H9F	GMA6C@	IB-H	A I F & \$ * \$ 7 H
Device marking code			MUR2060CT
Repetitive Peak Reverse Voltage			
Average Rectified Output Current @60Hz sine wave, R-load, T _c (FIG.1)	I _o	A	20
Surge(Non-repetitive)Forward Current @60Hz half sine-wave, 1 cycle, T _j =25	I _{FSM}	A	120
Current Squared Time @1ms t 8.3ms T _j =25	I ² t	A ² s	60
Storage Temperature	T _{stg}		
Typical Junction capacitance @4V,1MHz	C _j	pF	40

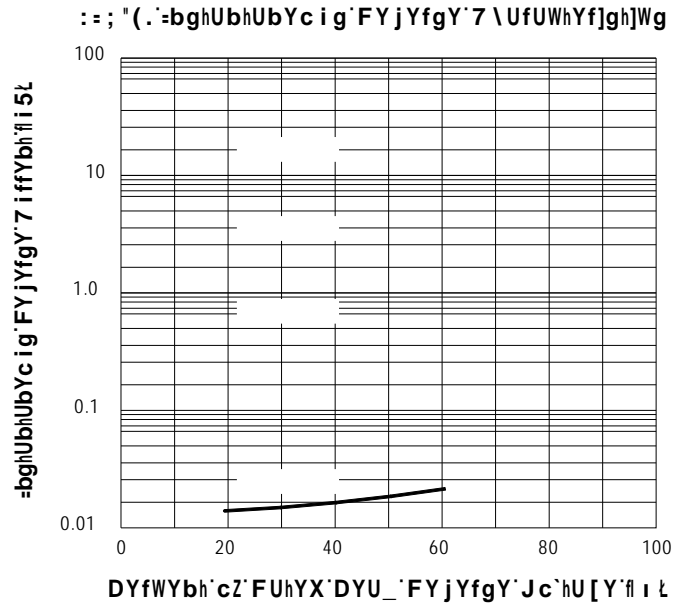
Adopt FRED chip
 Low forward Voltage drop
 Fast reverse recovery time
 High frequency operation
 High purity, high temperature epoxy encapsulation



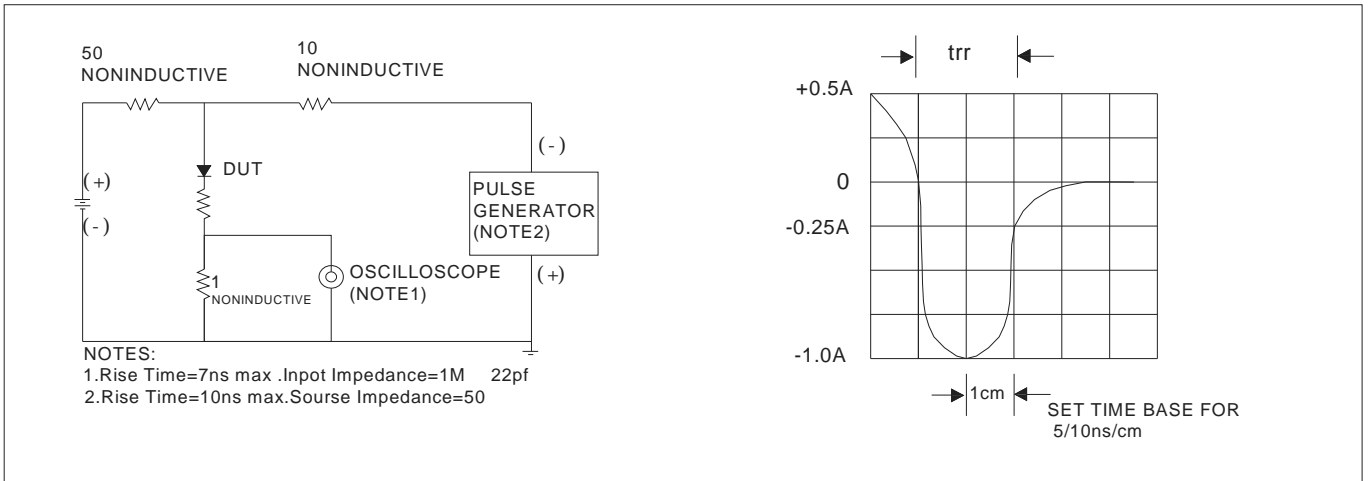
9`YWhf]WU`7\UfUWhYf]gh]Wg

D5F5A9H9F

GMA6C@ IB-H

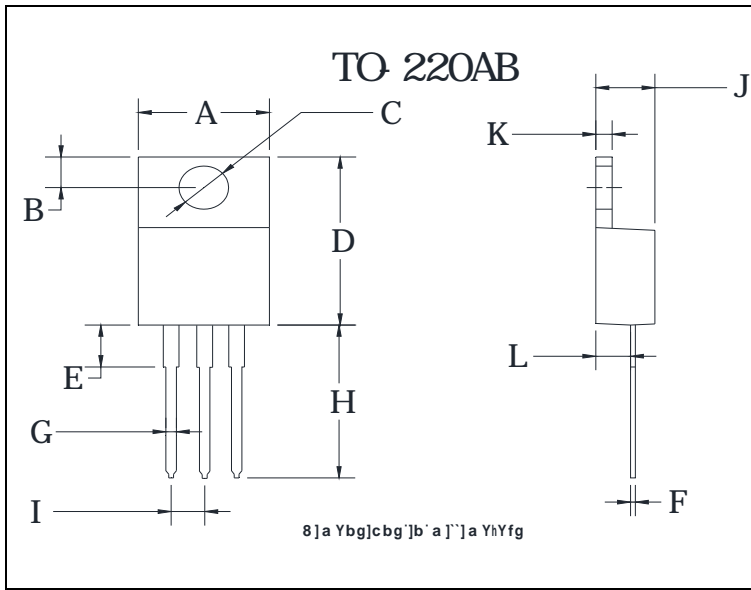


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C i h`]bY`8]a Ybg]cbg`



HC!&&\$56`

AIF&\$*\$7H
